

Roll No. \_\_\_\_\_

Total Printed Pages : **3****04BEE105****B.TECH.ELECTRICAL & ELECTRONICS ENGG.****IV- SEM Examination, May/June - 2016****SUB : LINEAR INTERGRATED CIRCUITS****Time : 3 Hours]****[Total Marks 60**

Use of following supporting material is permitted during examination.

1. \_\_\_\_\_ Nil \_\_\_\_\_      2. \_\_\_\_\_ Nil \_\_\_\_\_

*Note: 1. Attempt any five questions.**2. Each question carry equal marks.***UNIT-I**

1. Derive the expressions for DC and AC analysis of dual input balanced output differential amplifier.

**OR**

1. Define non- inverting configuration of op-amp and derive the expressions for closed loop voltage gain ( $A_v$ ) and input resistance ( $R_i$ )